EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	983	716/8.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/30 12:34
S136	9	logical adj effort and (fanout fan adj2 out) with optim$7 and (siz$3 width) with model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 14:15
S135	12	logical adj effort and (fanout fan adj2 out) and optim$7 and (siz$3 width) with model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:50
S134	23	logical adj effort and (fanout fan adj2 out) and optim$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:49
S133	3	logical adj effort and (fanout fan adj2 out) and resistor and (vary varied varies varying dynamic$4) and model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:48
S132	3	logical adj effort and (fanout fan adj2 out) and resistor and (vary varied varies varying dynamic$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:40
S131	0	logical adj effort and (fanout fan adj2 out) and resistor same (varied varying dynamic$4) and model$3 and steady adj2 state	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:37
S130	0	logical adj effort and (fanout fan adj2 out) and resistor same dynamic$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:34

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S12 9	0	logical adj effort and (fanout fan adj2 out) and resistor with dynamic$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:34
S12 8	0	logical adj effort and (fanout fan adj2 out) and resistor near3 dynamic$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/27 13:33
S12 7	1602	716/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:16
S12 6	0	716/5.ccls. and logical adj effort and model$3 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:15
S12 5	6	716/5.ccls. and logical adj effort and model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:14
S12 4	6	716/5.ccls. and logical adj effort	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:14
S12 3	1602	716/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:13
S12 2	0	(logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj2 state and solution and (logic adj gate transistor)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:11

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S12 1		1	(logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj2 state and solution and (logic adj gate transistor))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:05
S12 0		1	(logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj2 state and solution and transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:05
S11 9		0	(logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj2 state and solution and transistor).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:05
S11 8		0	(logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj2 state and solution).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:04
S11 7		1	logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj2 state and solution	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 11:03
S11 6		1	logical adj effort and (fanout fan adj2 out) and siz$3 and model$3 and resistor and steady adj state same solution	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 10:56
S11 5		1	logical adj effort and (fanout fan adj2 out) and siz$3 same model$3 same resistor and steady adj state same solution	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/24 10:56
S11 4		8	logical adj effort and resistor and model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/23 16:49

S11 3	2	logical adj effort and fanout and resistor and model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/23 16:49
S11 2	1	logical adj effort and fanout and resistor same model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/23 16:48
S10 8	1	logical adj effort and fanout and resistor same (siz$3 width) same model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/23 16:48
S10 7	0	logical adj effort same fanout same resistor same (siz$3 width) same model$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/23 16:46
S10 1	0	(siz$3 same (fan-out fan adj out) same model$3 same resistor same (vari$4 vary$3 chang$3 tun$4 adjust$3) same value same (steady adj state)same transistor).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:49
S10 3	0	siz$3 same (fan-out fan adj out) same model$3 same resistor same (vari$4 vary$3 chang$3 tun$4 adjust$3) same value same (steady adj state) and logical adj effort	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:48
S10 2	0	siz$3 same (fan-out fan adj out) same model$3 same resistor same (vari$4 vary$3 chang$3 tun$4 adjust$3) same value same (steady adj state) and logical adj effort	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:47
S94	0	S87 and S88 and S90 and logical adj effort	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:34

S93	21	S87 and S88 and S90	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:31
S92	0	S86 and S88 and S90	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:30
S91	2	S86 and S88	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:28
S90	127028	resistor same (vari$4 vary$3 chang$3) same value	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:26
S88	72472	transistor same siz$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:21
S87	3902	(fan-out or fan adj out) and optim$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:20
S86	7	(fan-out or fan adj out) and (logical adj effort)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:20
S85	3905	(fan-out or fan adj out) and (logical adj effort optim$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:19

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S84	1	(fan-out or fan adj out) and optim$7 and resistor and transistor and siz$3 and logical adj effort	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:18
S83	0	(fan-out or fan adj out) same optim$7 and resistor and transistor and siz$3 and logical adj effort	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:15
S82	38	(fan-out or fan adj out) same optim$7 and resistor and transistor and siz$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:15
S81	50	(fan-out or fan adj out) same optim$7 and resistor and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:11
S80	79	(fan-out or fan adj out) same optim$7 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:11
S79	575	(fan-out or fan adj out) same optim$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 12:10
S78	9	716/2.ccls. and (fan adj out fan-out) and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 11:20
S77	102	716/2.ccls. and (fan adj out fan-out)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 11:18

S76	1174	716/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 11:09
S75	0	716/2.ccls. and (fan adj out fan-out) and (logical adj effort) and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 11:00
S74	0	716/5.ccls. and (fan adj out fan-out) and (logical adj effort) and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 10:59
S73	2	716/5.ccls. and (fan adj out fan-out) and (logical adj effort)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 10:59
S72	1562	716/5.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/08 10:56